

SSCE3V311N7

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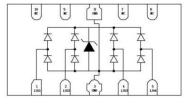
Ultra Low Capacitance Array for ESD Protection

• Description

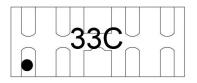
The SSCE3V311N7 provides a typical line to line capacitance of 0.3pF between I/O pins and low insertion loss up to 3.0GHz providing greater signal integrity making it ideally suited for HDMI applications, such as Digital TVs, DVD players, Computing, set-top boxes and MDDI applications in mobile computing devices.

It has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from overvoltage caused by ESD(electrostatic discharge), CDE(Cable Discharge Events),and EFT (electrical fast transients).

PIN configuration



<u>Top view</u>



Marking

• Feature

- ♦ 50W peak pulse power ($t_P = 8/20\mu s$)
- ♦ DFN2510-10L Package
- ♦ Working voltage: 3.3V
- ♦ Low clamping voltage
- ♦ Low capacitance
- RoHS compliant transient protection for high-speed data
- ♦ Complies with following standards: -IEC61000-4-2(ESD) ±20kV(air), -IEC61000-4-2(ESD) ±15kV(contact)

Applications

- ♦ DVI & HDMI Port Protection
- ♦ Serial and Parallel Ports
- ♦ Projection TV
- ♦ Notebooks, Desktops, Server
- ♦ USB 1.1/2.0/3.0/3.1/OTG

Mechanical data

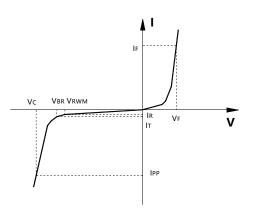
- ♦ Lead finish:100% matte Sn (Tin)
- ♦ Mounting position: Any
- ♦ Qualified max reflow temperature:260°C
- ♦ Device meets MSL 3 requirements
- ♦ Pure tin plating: $7 \sim 17$ um
- ♦ Pin flatness: ≤3mil



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• Electronic Parameter

Symbol	Parameter	
V _{RWM}	Peak Reverse Working Voltage	
IR	Reverse Leakage Current @ VRWM	
V _{BR}	Breakdown Voltage @ I⊤	
IT	Test Current	
IPP	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
P _{PP}	Peak Pulse Power	
CJ	Junction Capacitance	



● Absolute maximum rating @TA=25℃

Parameter	Symbol	Value	Units
Peak Pulse Power (8/20µs)	P _{PP}	50	W
Peak Pulse Current(8/20µs)	IPP	5	A
Storage Temperature	T _{STG}	-55/+150	°C
Operating Temperature	TJ	-55/+150	°C

• Electrical Characteristics @TA=25°C

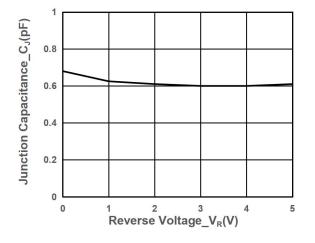
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}	Any I/O to GND			3.3	V
Breakdown Voltage	V_{BR}	I⊤= 1mA Any I/O to GND	3.8			V
Reverse Leakage Current	I _R	V _{RWM} =3.3V			0.1	μA
Clamping Voltage	Vc	I _{PP} =1Α, t _P = 8/20μs			6.5	V
Clamping Voltage	Vc	I _{PP} =5A, t _P = 8/20µs			10	V
ESD Clamping Voltage(Note1)	V _{CL-ESD}	IEC 61000-4-2+ 8kV(I _{TLP} =16A),contact mode,T=25℃,any I/O pin to GND		22		V
Dynamic resistance	R _{DYN}			0.75		Ω
Junction Capacitance	CJ	V _R = 0V, f = 1MHz, between I/O pins		0.3	0.4	pF
		V _R = 0V, f = 1MHz, any I/O pin to GND			0.8	pF

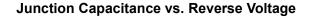
Note 1: ESD Clamping Voltage was measured by Transmission Line Pulsing (TLP) System. TLP conditions: Z_0 =50 Ω , t_p = 100ns, t_r = 1ns.

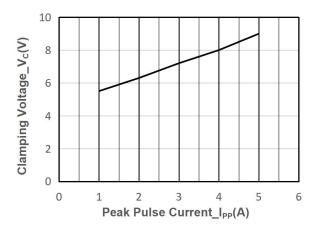


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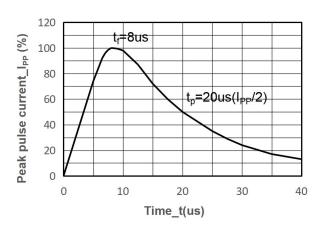
• Typical Performance Characteristics



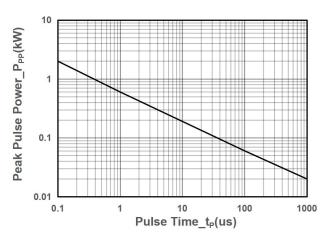




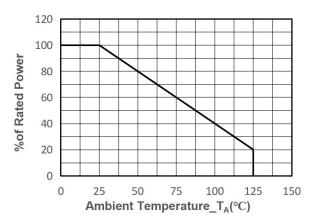
Clamping Voltage vs. Peak Pulse Current



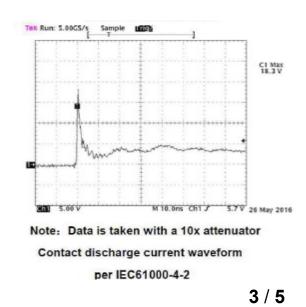
8/20us Pulse Waveform



Peak Pulse Power vs. Pulse Time



Power derating vs. Ambient temperature





• Package Information

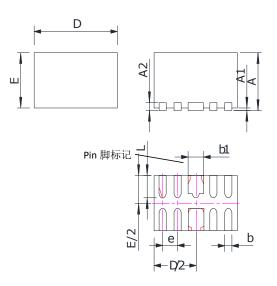
Ordering Information

Device	Package	Qty per Reel	Reel Size
SSCE3V311N7	DFN2510-10L	3000	7 Inch

Mechanical Data

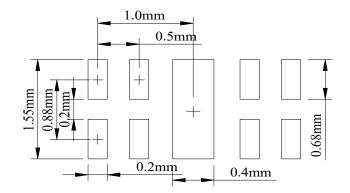
Case: DFN2510-10L

Case Material: Molded Plastic. UL Flammability



DIM	Millimeters		
	Min	Max	
Α	0.45	0.65	
A1	0.05REF		
A2	0.15REF		
b	0.15	0.25	
b1	0.30	0.50	
D	2.424	2.576	
E	0.924	1.076	
е	0.50REF		
L	0.30	0.45	

Recommended Pad outline





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